Table of contents

- **Design Methods**
- **CMOS Chip Design Options**
- **Design Capture Tools**
- **Design Verification Tools**
- **CMOS Test Methodolgies**
- **Introduction**
- **Fault Models**
- **Design for Testability**
- **Automatic Test Pattern Generation**
- **Design for Manufacturability**
- **CMOS Subsystem Design**
- **Introduction**
- **Adders and Related Functions**
- **Binary Counters**
- **Multipliers and Filter Structures**
- **Random Access and Serial Memory**
- **Datapaths**
- **FIR and IIR Filters**
- **Finite State Machines**
- **Programmable Logic Arrays**
- **Random Control Logic**