

- Preface p. xvii
- 1 Introduction p. 1
- 1.1 Objectives and Brief History p. 1
- 1.2 Frequency Spectra and Atmospheric Propagation Loss p. 4
- 1.3 Wireless Applications p. 5
- 1.4 Organization of This Book p. 6
- References p. 7
- 2 General Wireless Systems p. 9
- 2.1 Introduction p. 9
- 2.2 Wireless System Frequency Allocations p. 10
- 2.2.1 Cellular Radio Systems p. 10
- 2.2.2 Cordless Telephony p. 11
- 2.2.3 Mobile Satellite Systems p. 13
- 2.3 Cellular Systems in the United States, Europe, and Japan p. 13
- 2.3.1 Analog Cellular Systems p. 13
- 2.3.2 Digital Cellular Systems p. 14
- 2.3.3 Code Division Multiple Access Systems p. 17
- 2.4 Cordless Telephony p. 19
- 2.4.1 Analog Cordless Phones p. 19
- 2.4.2 Digital Cordless Telephones p. 20
- 2.4.3 WACS/PACS p. 21
- 2.4.4 DCS 1800 p. 22
- 2.5 Wireless LAN Systems p. 22
- 2.6 Satellite Communication Systems p. 23
- 2.6.1 Iridium System p. 24
- 2.6.2 Globalstar System p. 26
- 2.6.3 ICO-P System p. 27
- 2.7 Future Wireless Systems p. 29
- 2.7.1 Bluetooth Systems p. 29
- 2.7.2 PCN/PCS Systems p. 31
- 2.7.3 Third Generation Cellular Systems p. 33
- 2.7.4 MMDS/LMDS p. 33
- References p. 35
- 3 Overview of Active Devices and Circuit Technologies p. 37
- 3.1 Active Device Types p. 37
- 3.2 Circuit Types and Their Fabrication p. 42
- 3.3 Active Device Operation p. 43
- 3.3.1 Si Bipolar Transistor p. 43
- 3.3.2 GaAs MESFET p. 49
- 3.3.3 Heterojunction Field-Effect Transistors p. 55
- 3.3.4 Heterojunction Bipolar Transistors p. 61
- 3.3.5 Future Trend p. 70
- 3.4 Circuit Manufacturing Technologies p. 70
- 3.4.1 Printed Circuit Board p. 72
- 3.4.2 Hybrid Integrated Circuit p. 73
- 3.4.3 Monolithic Integrated Circuit p. 78

- 3.4.4 Multichip Modules p. 81
- 3.4.5 Technology Comparison and Choices p. 81
- References p. 83
- 4 Transmitter and Receiver System Parameters p. 87
- 4.1 Introduction p. 87
- 4.2 Receiver System Considerations p. 87
- 4.2.1 Natural Sources of Receiver Noise p. 89
- 4.2.2 Receiver Noise Figure p. 91
- 4.2.3 Equivalent Noise Temperature p. 93
- 4.2.4 Dynamic Range, 1dB Compression Point, and Minimum Detectable Signal p. 93
- 4.2.5 Third-Order Intercept Point and Intermodulation Products p. 95
- 4.3 Transmitter System Considerations p. 99
- 4.3.1 Transmitter Noise p. 100
- 4.3.2 Adjacent Channel Power Ratio p. 102
- References p. 103
- 5 Transmission Lines and Impedance Matching Techniques p. 105
- 5.1 Introduction p. 105
- 5.2 Transmission Line Equation p. 106
- 5.3 Reflection, Transmission, and Impedance p. 110
- 5.4 Voltage Standing-Wave Ratio p. 112
- 5.5 Smith Charts p. 116
- 5.6 S-Parameters p. 122
- 5.7 Wires and Twin Lines p. 124
- 5.8 Coaxial Lines p. 125
- 5.9 Microstrip Lines p. 128
- 5.9.1 Analysis Formulas p. 130
- 5.9.2 Synthesis Formulas p. 130
- 5.9.3 Effects of Strip Thickness p. 131
- 5.9.4 Effects of Enclosure or Shield p. 132
- 5.9.5 Graphical Method p. 132
- 5.9.6 Effects of Dispersion p. 132
- 5.9.7 Losses p. 135
- 5.10 Other Planar Transmission Lines p. 136
- 5.10.1 Striplines p. 136
- 5.10.2 Coplanar Waveguides and Coplanar Striplines p. 138
- 5.10.3 Slotlines p. 139
- 5.11 Waveguides p. 141
- 5.11.1 TE Modes p. 144
- 5.11.2 TE<sub>10</sub> Mode p. 145
- 5.11.3 TM Modes p. 146
- 5.12 Lumped Elements p. 146
- 5.12.1 Lumped Resistors p. 147
- 5.12.2 Lumped Capacitors p. 149
- 5.12.3 Lumped Inductors p. 151
- 5.13 Impedance Matching Networks p. 154
- 5.13.1 Matching Stubs p. 155

- 5.13.2 Single-Section Quarter-Wavelength Transformer p. 155
- 5.13.3 Lumped Elements p. 156
- 5.13.4 Multisection Quarter-Wavelength Transformers p. 159
- 5.13.5 Tapered Transformers p. 162
- 5.14 Discontinuities p. 164
- References p. 169
- 6 Filters and Couplers p. 171
- 6.1 Filters p. 171
- 6.1.1 Filter Parameter Definition p. 171
- 6.1.2 Types of Filters p. 174
- 6.1.3 Requirements for Wireless Applications p. 174
- 6.2 Filter Synthesis p. 174
- 6.2.1 Traditional Filter Design Approach p. 175
- 6.2.2 Solutions to Low-Pass Prototype Design p. 176
- 6.2.3 Filter Design From Prototype Low-Pass Filter p. 180
- 6.2.4 Bandpass Filter Design Using Coupling Coefficients p. 184
- 6.3 Filter Realizations p. 185
- 6.3.1 Lumped Element Filters p. 186
- 6.3.2 Coaxial Filters p. 186
- 6.3.3 Microstrip Line Filters p. 190
- 6.3.4 SAW Filters p. 193
- 6.4 Couplers p. 196
- 6.4.1 Coupler Parameter Definition p. 197
- 6.4.2 Coupler Requirements for Wireless Applications p. 198
- 6.5 Design of Couplers p. 199
- 6.5.1 90[degree] Hybrid p. 199
- 6.5.2 Rat-Race Hybrid p. 203
- 6.5.3 Coupled Line Directional Couplers p. 206
- 6.5.4 Other Couplers p. 211
- References p. 215
- 7 Switches p. 221
- 7.1 Introduction p. 221
- 7.1.1 Switch Parameter Definitions p. 221
- 7.1.2 Devices for Switches p. 222
- 7.1.3 Basic Requirements of a Switch for Wireless Applications p. 229
- 7.2 Design of Switches p. 229
- 7.2.1 Device Models p. 229
- 7.2.2 Types of Switches p. 234
- 7.2.3 Switch Configurations p. 237
- 7.2.4 Basic Theory of Switches p. 240
- 7.3 Multiport Switches p. 243
- 7.3.1 Multithrow Switches p. 243
- 7.3.2 Matrix Switches p. 243
- 7.3.3 Diversity Switch p. 244
- 7.4 High Isolation Switches p. 245
- 7.5 Broadband Switches p. 248

- 7.6 High Power Switches p. 250
- 7.6.1 Impedance Transformation Technique p. 253
- 7.6.2 Stacked FETs Method p. 254
- 7.6.3 Resonant Circuit Technique p. 255
- 7.6.4 Power Handling of PIN Diode Switches p. 258
- 7.7 Low Distortion Switches p. 259
- 7.8 Switching Speed p. 262
- 7.9 Biasing of Switching Devices p. 263
- 7.9.1 Biasing of PIN Diodes p. 263
- 7.9.2 Biasing of FETs p. 265
- 7.9.3 Single Positive Power Supply Operation p. 265
- 7.9.4 Switches With Integrated Control p. 266
- References p. 267
- 8 Low-Noise Amplifiers p. 271
- 8.1 Introduction p. 271
- 8.2 Two-Port Description of a Device p. 271
- 8.2.1 Noise Modeling p. 272
- 8.2.2 Low-Noise Behavior of BJTs and MESFETs p. 275
- 8.3 Circuit Design Considerations p. 279
- 8.4 Amplifier Power Gain p. 279
- 8.4.1 Transducer Power Gain p. 280
- 8.5 Amplifier Stability p. 283
- 8.5.1 Stability Circles p. 283
- 8.5.2 Constant Gain Circles p. 287
- 8.5.3 Constant Noise Figure Circles p. 290
- 8.6 Small Signal Amplifier Design Procedure p. 292
- 8.7 Narrow Band Amplifiers p. 293
- 8.7.1 Maximum Power Gain Design p. 293
- 8.7.2 Low-Noise Amplifier Design p. 294
- 8.8 Low-Noise Amplifiers for Wireless Applications p. 295
- 8.8.1 Silicon Low-Power LNAs p. 297
- 8.8.2 GaAs Low-Power LNAs p. 301
- 8.8.3 HBT Low-Noise Amplifiers p. 308
- References p. 310
- 9 Mixers p. 313
- 9.1 Introduction p. 313
- 9.2 Mixer Fundamentals p. 313
- 9.3 Down Converting and Up Converting Mixers p. 314
- 9.4 System Considerations p. 315
- 9.5 Devices for Mixers p. 317
- 9.5.1 Diode Mixer Theory and Operation p. 317
- 9.5.2 FET Mixer Theory and Operation p. 322
- 9.5.3 Dual-Gate FET (DGFET) Mixer p. 327
- 9.6 Mixer Classifications p. 330
- 9.6.1 Single-Ended Mixers p. 330
- 9.6.2 Balanced Mixers p. 331

- 9.6.3 Couplers p. 332
- 9.6.4 Single Balanced Mixers (SB Mixers) p. 339
- 9.6.5 Double Balanced Mixers (DB Mixers) p. 341
- 9.6.6 Image Reject Mixers p. 344
- 9.6.7 Gilbert Cell Mixers p. 346
- 9.7 Monolithic Low-Power Mixer Examples p. 347
- 9.7.1 Introduction p. 347
- 9.7.2 Mixer Implementation in Silicon Technologies p. 347
- 9.7.3 GaAs MESFET Mixers p. 352
- 9.7.4 Mixer Implementation in HEMTs p. 354
- 9.8 Monolithic Low-Power Downconverters p. 355
- 9.8.1 Downconverter Characterization p. 357
- 9.8.2 Design Examples p. 358
- References p. 363
- 10 Oscillators and Modulation p. 367
- 10.1 Introduction p. 367
- 10.2 Oscillator Characteristics p. 367
- 10.3 General Theory p. 368
- 10.4 Oscillators Using Gunn and IMPATT Devices p. 370
- 10.5 Small-Signal Design for Transistor Oscillators p. 375
- 10.6 Large-Signal Design for Transistor Oscillators p. 379
- 10.7 Transistor Oscillator Circuits p. 379
- 10.7.1 Feedback Oscillators p. 379
- 10.7.2 Reflection-Type Oscillators p. 381
- 10.7.3 Stable Dielectric Resonator Oscillators p. 381
- 10.8 Voltage-Tunable Transistor Oscillators p. 384
- 10.9 Noise In Transistor Oscillators p. 387
- 10.10 Crystal Oscillators p. 390
- 10.11 Phase-Locked Oscillators p. 392
- 10.12 Frequency Synthesizers p. 393
- 10.13 Modulation and Demodulation Techniques p. 394
- 10.13.1 Analog Modulation and Demodulation p. 395
- 10.13.2 Digital Modulation p. 397
- References p. 402
- 11 Power Amplifiers p. 405
- 11.1 Introduction p. 405
- 11.1.1 Amplifier Parameter Definitions p. 406
- 11.1.2 Classes of Power Amplifiers p. 410
- 11.1.3 Power Amplifier Requirements p. 413
- 11.1.4 Device/Circuit Technologies p. 415
- 11.2 Device Models--Linear and Nonlinear p. 415
- 11.2.1 Linear Model p. 415
- 11.2.2 Nonlinear Model p. 416
- 11.3 Power Amplifier Design p. 418
- 11.3.1 Device Type and Size p. 418
- 11.3.2 Device Models and Loadpull Data p. 419

- 11.3.3 Design Methodology p. 422
- 11.3.4 Matching Networks p. 424
- 11.3.5 Biasing Power Amplifiers p. 425
- 11.3.6 Fabrication Technology p. 426
- 11.3.7 Stability Analysis p. 427
- 11.3.8 Thermal Design p. 433
- 11.4 Design of Linear Power Amplifiers p. 434
- 11.4.1 Predistortion Techniques p. 435
- 11.4.2 Linear Amplifier Design Example p. 435
- 11.5 High Efficiency Amplifiers p. 437
- 11.5.1 Class B Design p. 437
- 11.5.2 Class E Design p. 443
- 11.5.3 Class F Design p. 448
- 11.5.4 Comparison Between Various Classes p. 452
- 11.6 Power Combining Techniques p. 454
- 11.7 Measurements p. 458
- 11.7.1 Loadpull Measurements p. 458
- 11.7.2 IP3/IM3 p. 463
- 11.7.3 ACPR Measurement p. 465
- References p. 466
- 12 Antennas p. 473
- 12.1 Introduction p. 473
- 12.2 Isotropic Radiator, Plane Waves, and Far-Field Region p. 475
- 12.3 Antenna Analysis p. 478
- 12.4 Antenna Characteristics and Parameters p. 479
- 12.4.1 Input VSWR and Input Impedance p. 479
- 12.4.2 Bandwidth p. 480
- 12.4.3 Power Radiation Patterns p. 480
- 12.4.4 Half-Power Beamwidth and Sidelobe Level p. 481
- 12.4.5 Directivity, Gain, and Efficiency p. 481
- 12.4.6 Polarization and Cross-Polarization Level p. 483
- 12.4.7 Effective Area p. 484
- 12.4.8 Beam Efficiency p. 484
- 12.4.9 Back Radiation p. 484
- 12.4.10 Estimation of High-Gain Antennas p. 484
- 12.5 Monopole and Dipole Antennas p. 484
- 12.5.1 Radiation Patterns and Directivity p. 486
- 12.5.2 Input Impedance p. 487
- 12.5.3 Whip, Folded, Sleeve, and Inverted Monopoles p. 488
- 12.6 Loop Antennas p. 490
- 12.7 Horn Antennas p. 492
- 12.8 Parabolic Dish Antennas p. 494
- 12.9 Microstrip Patch Antennas p. 496
- 12.9.1 Patch Antenna Design Using Transmission Line Model p. 497
- 12.9.2 Radiation Patterns and Feeding Methods p. 500
- 12.10 Other Antennas p. 504

- 12.11 Antenna Arrays and Phased Arrays p. 506
- References p. 511
- Index p. 513